



## Call for Papers

The Mexican Society for Computer Science (SMCC), in technical cooperation with the IEEE and IEEE Computer Society, invites researchers to submit research papers to the [International Conference on Reconfigurable Computing and FPGAs 2005, ReConFig'05](#), to be held at the city of Puebla, Mexico, on September 28-30, 2005. The conference will be held in parallel with the [International Conference in Computer Science 2005, ENC'05](#).

The aim of the conference is to bring together researchers, students, and industry working on all aspects of reconfigurable computing and FPGA devices. The conference seeks to promote the use of programmable logic for research and education, and also to create links among different research groups by offering a forum where results of their latest research work can be presented.

**Conference Topics.** ReConFig'05 covers a broad spectrum of topics including, but not limited to:

### Architectures

- Reconfigurable architectures
- Custom computer systems
- Systems on reconfigurable chip (SoRC)
- Embedded systems
- Fault tolerant systems
- Hardware inspired on biologic structures, neural networks and genetic algorithms
- Low power reconfigurable architectures

### Tools

- Reconfiguration techniques, dynamic reconfiguration, support and applications
- Languages and compiling techniques
- Simulations and programming environments for FPGAs (CAD)
- UML and HDLs
- Hardware/software libraries
- Verifications and test of reconfigurable circuits and systems
- Hardware/software codesign and cosimulation

### Applications

- Communications and network applications
- Mobile applications
- Digital signal processing applications
- Algorithms implemented on FPGAs
- Rapid prototyping

### Education

- Courses and laboratories on programmable devices, FPGAs and reconfigurable computing
- HDLs, FPGAs and reconfigurable computing tutorials
- Interactive tutorials and on-line courses
- Integrating reconfigurable systems into university curricula
- Discussions about the potential of FPGAs and reconfigurable computing

### Conference Proceedings

Conference proceedings by IEEE Computer Society Press

### Paper Submission Instructions

Electronic submissions of full papers are due on May 31st, 2005. Full papers should have a length of up to 8 pages and must be written in English. They have to be submitted for evaluation as a PDF or PostScript file through the ReConFig05 web page. Further instructions for electronic submission and paper formatting will be published in the conference site <http://ccc.inaoep.mx/fpgacentral/reconfig/2005/>

### Important dates

<b>Paper Submission</b>	May 31, 2005
<b>Acceptance Notification</b>	July 1, 2005
<b>Camera-Ready Papers</b>	July 12, 2005
<b>Conference</b>	September 28– 30, 2005

**For more information and news, please visit:** <http://ccc.inaoep.mx/fpgacentral/reconfig/2005/>